

BRIEF DESCRIPTION OF THE DRAWINGS

The present invention is illustrated by way of example and not limitation in the accompanying figures.

Figure 1a illustrates a prior art organization of memory.

5 Figure 1b illustrates an organization of memory according to one embodiment of the present invention.

Figure 1c illustrates an organization of memory according to an alternative embodiment of the present invention.

10 Figure 2 illustrates an implementation of one embodiment of the present invention.

Figure 3 illustrates an implementation of an alternative embodiment of the present invention.

Figure 4A illustrates an 8x16 character in one embodiment.

15 Figure 4B illustrates an 8x14 character produced from the character of Figure 4A by one embodiment.

Figure 4C illustrates another 8x16 character in one embodiment.

Figure 4D illustrates an 8x14 character produced from the character of Figure 4C by one embodiment.

Figure 4E illustrates yet another 8x16 character in one embodiment.

20 Figure 4F illustrates an 8x14 character produced from the character of Figure 4E by one embodiment.

Figure 4G illustrates a pair of 8x16 characters in one embodiment.

25 Figure 4^H~~D~~ illustrates the pair of 8x14 characters produced from the characters of Figure 4G by one embodiment.

Figure 5a illustrates a functional block diagram of an embodiment of the present invention.